

(>Bin) or Part Name	Direction	Forced Assembly Strategy?	Forced Relative Overhang Position?	Direct Synthesis Firewall?	Extra 5' CPEC overlap bps	Extra 3' CPEC overlap bps
<b>&gt;vector_backbone</b>						
pS8c-vector_backbone	forward					
<b>&gt;nterm_sig_pep</b>		<b>TRUE</b>				
BMC_nterm_sig_pep	forward					
ccmN_nterm_sig_pep	forward					
<b>&gt;gly_ser_linker</b>						
long_gly_ser_linker	forward					
short_gly_ser_linker	forward					
<b>&gt;GFPuv</b>						
GFPuv	forward					
<b>&gt;ssrA_tag_5prime</b>		<b>Embed_in_primer_reverse</b>			<b>TRUE</b>	
ssrA_tag_5prime	forward	Embed_in_primer_reverse				
ssrA_tag_enhanced_5prime	forward	Embed_in_primer_reverse				
<b>&gt;ssrA_tag_3prime</b>		<b>Embed_in_primer_forward</b>				
ssrA_tag_3prime	forward	Embed_in_primer_forward				