

(>Bin) or Part Name	Direction	Forced Assembly Strategy?	Forced Relative Overhang Position?	Direct Synthesis Firewall?	Extra 5' CPEC overlap bps	Extra 3' CPEC overlap bps
pS8c-vector_backbone	forward		2			
BMC_nterm_sig_pep	forward			TRUE		
long_gly_ser_linker	forward					
GFPuv	forward	unspecified	2			
ssrA_tag_5prime	forward	Embed_in_primer_reverse		TRUE		
ssrA_tag_3prime	forward	Embed_in_primer_forward				