

(>Bin) or Part Name	Direction	Forced Assembly Strategy?	Forced Relative Overhang Position?	Direct Synthesis Firewall?	Extra 5' CPEC overlap bps	Extra 3' CPEC overlap bps
pBbS8c-RFP_EcoRI_XhoI_vector_backbone	forward	DIGEST				
RBS	forward					
gfpuv_sigpep_5prime	forward					
gfpuv_sigpep_XhoI_silent	forward					
gfpuv_sigpep_middle	forward	Direct Synthesis				
gfpuv_sigpep_BamHI_silent	forward					
gfpuv_sigpep_3prime	forward					